

L Number	Hits	Search Text	DB	Time stamp
1	1655337	display\$1	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/09/01 13:00
2	1414614	CRT\$1 OLED\$1 LCD\$1 PDP\$1 FED\$1	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/09/01 13:02
3	310071	display\$1 & (CRT\$1 OLED\$1 LCD\$1 PDP\$1 FED\$1)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/09/01 13:03
4	2609	(display\$1 & (CRT\$1 OLED\$1 LCD\$1 PDP\$1 FED\$1)) & ((plurality near3 pixel\$1) with matrix)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/09/01 13:29
5	2288617	switch\$3	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/09/01 13:04
6	2046	((display\$1 & (CRT\$1 OLED\$1 LCD\$1 PDP\$1 FED\$1)) & ((plurality near3 pixel\$1) with matrix)) & switch\$3	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/09/01 13:05
7	2014	((display\$1 & (CRT\$1 OLED\$1 LCD\$1 PDP\$1 FED\$1)) & ((plurality near3 pixel\$1) with matrix)) & switch\$3 & (row\$1 column\$1 scan\$4 data gate)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/09/01 13:07
8	92	(capacitor\$1 with (connect\$3 coupl\$3) with between with switch\$3) & (((display\$1 & (CRT\$1 OLED\$1 LCD\$1 PDP\$1 FED\$1)) & ((plurality near3 pixel\$1) with matrix)) & switch\$3) & (row\$1 column\$1 scan\$4 data gate))	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/09/01 13:11
9	865	aging near3 circuit\$3	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/09/01 13:10
10	1	((capacitor\$1 with (connect\$3 coupl\$3) with between with switch\$3) & (((display\$1 & (CRT\$1 OLED\$1 LCD\$1 PDP\$1 FED\$1)) & ((plurality near3 pixel\$1) with matrix)) & switch\$3) & (row\$1 column\$1 scan\$4 data gate))) & (aging near3 circuit\$3)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/09/01 13:10
11	293	(CRT\$1 OLED\$1 LCD\$1 PDP\$1 FED\$1) & (aging near3 circuit\$3)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/09/01 13:11
12	5	((display\$1 & (CRT\$1 OLED\$1 LCD\$1 PDP\$1 FED\$1)) & ((plurality near3 pixel\$1) with matrix)) & ((CRT\$1 OLED\$1 LCD\$1 PDP\$1 FED\$1) & (aging near3 circuit\$3))	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/09/01 13:11
13	5	switch\$3 & (((display\$1 & (CRT\$1 OLED\$1 LCD\$1 PDP\$1 FED\$1)) & ((plurality near3 pixel\$1) with matrix)) & ((CRT\$1 OLED\$1 LCD\$1 PDP\$1 FED\$1) & (aging near3 circuit\$3)))	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/09/01 13:11

15	2	(capacitor\$1 same (connect\$3 coupl\$3) with between with switch\$3) & (((((display\$1 & (CRT\$1 OLED\$1 LCD\$1 PDP\$1 FED\$1)) & ((plurality near3 pixel\$1) with matrix)) & switch\$3) & (row\$1 column\$1 scan\$4 data gate)) & (switch\$3 & (((display\$1 & (CRT\$1 OLED\$1 LCD\$1 PDP\$1 FED\$1)) & ((plurality near3 pixel\$1) with matrix)) & ((CRT\$1 OLED\$1 LCD\$1 PDP\$1 FED\$1) & (aging near3 circuit\$3))))))	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/09/01 13:13
16	2	(capacitor\$1 & (connect\$3 coupl\$3) with between with switch\$3) & (((((display\$1 & (CRT\$1 OLED\$1 LCD\$1 PDP\$1 FED\$1)) & ((plurality near3 pixel\$1) with matrix)) & switch\$3) & (row\$1 column\$1 scan\$4 data gate)) & (switch\$3 & (((display\$1 & (CRT\$1 OLED\$1 LCD\$1 PDP\$1 FED\$1)) & ((plurality near3 pixel\$1) with matrix)) & ((CRT\$1 OLED\$1 LCD\$1 PDP\$1 FED\$1) & (aging near3 circuit\$3))))))	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/09/01 13:17
17	2	"20040108518"	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/09/01 13:17
18	1	"20040108518" & aging	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/09/01 13:18
14	5	(((((display\$1 & (CRT\$1 OLED\$1 LCD\$1 PDP\$1 FED\$1)) & ((plurality near3 pixel\$1) with matrix)) & switch\$3) & (row\$1 column\$1 scan\$4 data gate)) & (switch\$3 & (((display\$1 & (CRT\$1 OLED\$1 LCD\$1 PDP\$1 FED\$1)) & ((plurality near3 pixel\$1) with matrix)) & ((CRT\$1 OLED\$1 LCD\$1 PDP\$1 FED\$1) & (aging near3 circuit\$3))))))	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/09/01 13:26
19	2	(((((display\$1 & (CRT\$1 OLED\$1 LCD\$1 PDP\$1 FED\$1)) & ((plurality near3 pixel\$1) with matrix)) & switch\$3) & (row\$1 column\$1 scan\$4 data gate)) & (switch\$3 & (((display\$1 & (CRT\$1 OLED\$1 LCD\$1 PDP\$1 FED\$1)) & ((plurality near3 pixel\$1) with matrix)) & ((CRT\$1 OLED\$1 LCD\$1 PDP\$1 FED\$1) & (aging near3 circuit\$3)))))) & prevent\$3	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/09/01 13:27
20	1	"20040108518" & prevent\$3	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/09/01 13:28
21	156	((CRT\$1 OLED\$1 LCD\$1 PDP\$1 FED\$1) & (aging near3 circuit\$3)) & prevent\$3	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/09/01 13:32
22	6	((CRT\$1 OLED\$1 LCD\$1 PDP\$1 FED\$1) & (aging near3 circuit\$3)) & prevent\$3) & (plurality near7 pixel\$1)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/09/01 13:29
23	74	((capacitor\$1 with (connect\$3 coupl\$3) with between with switch\$3) & (((((display\$1 & (CRT\$1 OLED\$1 LCD\$1 PDP\$1 FED\$1)) & ((plurality near3 pixel\$1) with matrix)) & switch\$3) & (row\$1 column\$1 scan\$4 data gate))) & prevent\$3	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/09/01 14:14

24	79	((capacitor\$1 with (connect\$3 coupl\$3) with between with switch\$3) & (((display\$1 & (CRT\$1 OLED\$1 LCD\$1 PDP\$1 FED\$1)) & ((plurality near3 pixel\$1) with matrix)) & switch\$3) & (row\$1 column\$1 scan\$4 data gate))) & (voltag\$3 with pixel\$1)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/09/01 13:33
25	75	((capacitor\$1 with (connect\$3 coupl\$3) with between with switch\$3) & (((display\$1 & (CRT\$1 OLED\$1 LCD\$1 PDP\$1 FED\$1)) & ((plurality near3 pixel\$1) with matrix)) & switch\$3) & (row\$1 column\$1 scan\$4 data gate))) & (voltag\$3 near12 pixel\$1)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/09/01 13:33
26	63	((capacitor\$1 with (connect\$3 coupl\$3) with between with switch\$3) & (((display\$1 & (CRT\$1 OLED\$1 LCD\$1 PDP\$1 FED\$1)) & ((plurality near3 pixel\$1) with matrix)) & switch\$3) & (row\$1 column\$1 scan\$4 data gate))) & (voltag\$3 near12 pixel\$1)) & prevent\$3	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/09/01 13:37
27	27	((capacitor\$1 with (connect\$3 coupl\$3) with between with switch\$3) & (((display\$1 & (CRT\$1 OLED\$1 LCD\$1 PDP\$1 FED\$1)) & ((plurality near3 pixel\$1) with matrix)) & switch\$3) & (row\$1 column\$1 scan\$4 data gate))) & (voltag\$3 near12 pixel\$1)) & prevent\$3) & organic	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/09/01 13:44
28	2	((capacitor\$1 with (connect\$3 coupl\$3) with between with switch\$3) & (((display\$1 & (CRT\$1 OLED\$1 LCD\$1 PDP\$1 FED\$1)) & ((plurality near3 pixel\$1) with matrix)) & switch\$3) & (row\$1 column\$1 scan\$4 data gate))) & (voltag\$3 near12 pixel\$1)) & prevent\$3) & organic) & (AC near7 voltag\$3)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/09/01 13:46
29	13	((capacitor\$1 with (connect\$3 coupl\$3) with between with switch\$3) & (((display\$1 & (CRT\$1 OLED\$1 LCD\$1 PDP\$1 FED\$1)) & ((plurality near3 pixel\$1) with matrix)) & switch\$3) & (row\$1 column\$1 scan\$4 data gate))) & (voltag\$3 near12 pixel\$1)) & (AC near7 voltag\$3)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/09/01 13:46
30	13	((capacitor\$1 with (connect\$3 coupl\$3) with between with switch\$3) & (((display\$1 & (CRT\$1 OLED\$1 LCD\$1 PDP\$1 FED\$1)) & ((plurality near3 pixel\$1) with matrix)) & switch\$3) & (row\$1 column\$1 scan\$4 data gate))) & (voltag\$3 near12 pixel\$1)) & (AC near7 voltag\$3)) & prevent\$3	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/09/01 14:28
31	2	((capacitor\$1 with (connect\$3 coupl\$3) with between with switch\$3) & (((display\$1 & (CRT\$1 OLED\$1 LCD\$1 PDP\$1 FED\$1)) & ((plurality near3 pixel\$1) with matrix)) & switch\$3) & (row\$1 column\$1 scan\$4 data gate))) & (voltag\$3 near12 pixel\$1)) & (AC near7 voltag\$3)) & prevent\$3) & organic	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/09/01 14:29